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DAC '92 Proceedings of the 29th ACM/IEEE Design Automation Conference Pages 518-523 Anaheim, California, USA June 08 - 12, 1992 IEEE Computer Society Press Los Alamitos, CA, USA ©1992 table of contents ISBN:0-89791-516-X

**Zero skew clock net routing - dl.acm.org**

Concurrent fault simulation of logic gates and memory blocks on message passing multicomputers (PDF) Bose , AT&T Bell Labs., Murray Hill, NJ, USA Agrawal , AT&T Bell Labs., Murray Hill, NJ, USA

**CSDL - 29th ACM/IEEE Design Automation Conference (DAC '92**

DAC '92 Proceedings of the 29th ACM/IEEE Design Automation Conference Pages 336-340 Anaheim, California, USA June 08 - 12, 1992 IEEE Computer Society Press Los Alamitos, CA, USA ©1992 table of contents ISBN:0-89791-516-X

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Title: Recurrence equations and the optimization of synchronous logic circuits - Design Automation Conference, 1992. Proceedings., 29th ACM/IEEE

**Recurrence equations and the optimization of synchronous**

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Analog DAC Users Manual Unique Product - a Preamp or a DAC? This product has the option to be a unique combination of a very high performance DAC and the ultimate passive preamp. It is intended to be used as both for the best possible sound in your system. Our preamp philosophy is based on the belief that from the moment analog audio is

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Title: Simulating lossy interconnect with high frequency nonidealities in linear time - Design Automation Conference, 1992. Proceedings., 29th ACM/IEEE

**Simulating lossy interconnect with high frequency**

8 fs interpolator multibit sigma-delta modulator atten/ mute dac reset 0 8457-001 figure 1.

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### **12-Channel, High Performance, Differential Output, 192 kHz**

Title: Specification partitioning for system design - Design Automation Conference, 1992. Proceedings., 29th ACM/IEEE Author: IEEE Created Date

### **Specification partitioning for system design - Design**

EVENT AUDIT 54th Design Automation Conference (DAC 2017) Audience Insights page 1 of 9 ... Company Name: Association for Computing Machinery (ACM) Electronic System Design Alliance (ESD Alliance) IEEE Council on Electronic Design Automation (CEDA) Address: 1721 Boxelder St., Ste. 107 Louisville, CO 80027

### **EVENT AUDIT - Hall Erickson, Inc.**

A/D and D/A Converter 171 Input Weights For the DAC of it should be noted that each digital input contributes a different amount to the analog output. This is easily seen if we examine the cases where only one input is HIGH Table 7.1. The contributions of each digital input are weighted according to their position in the binary number.

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